

FIG. 1

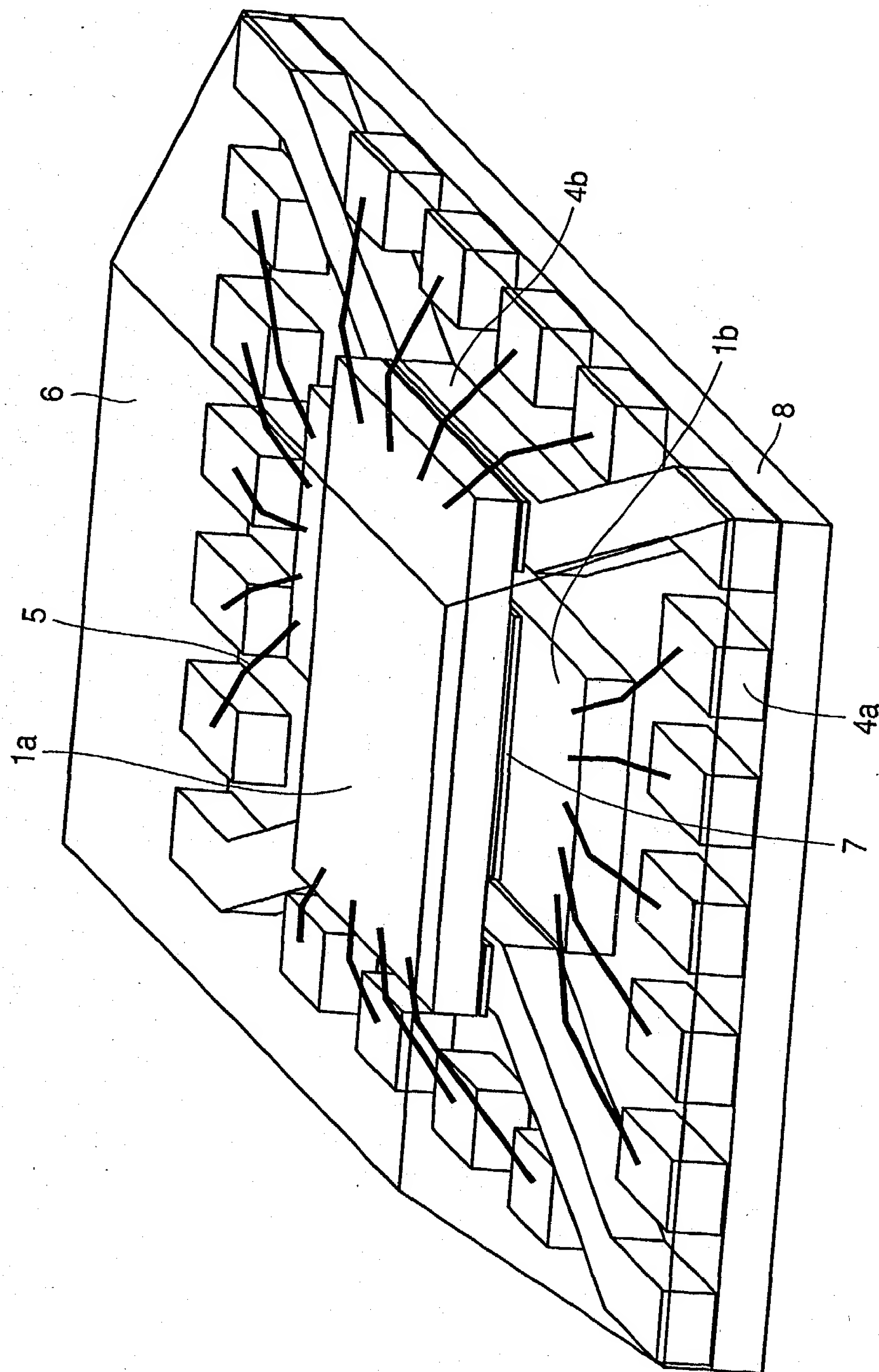


FIG. 2

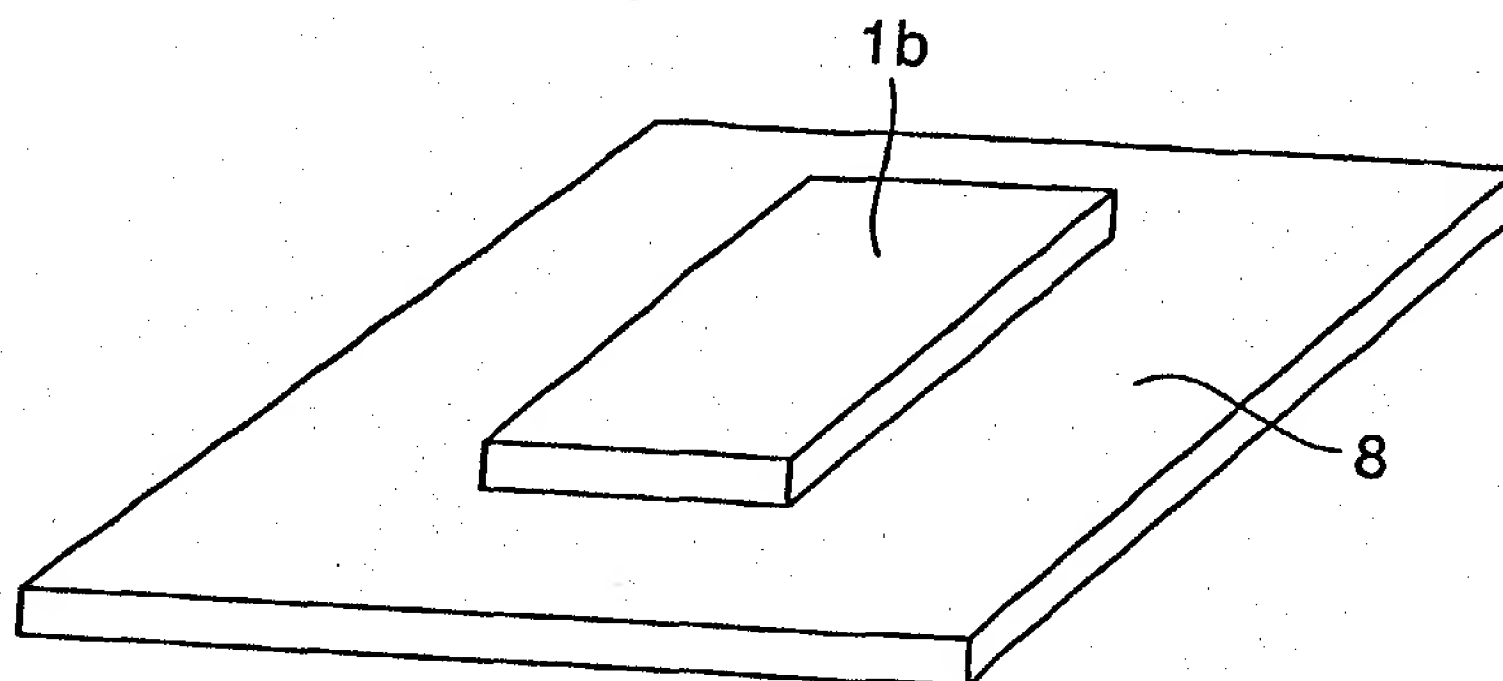


FIG. 3

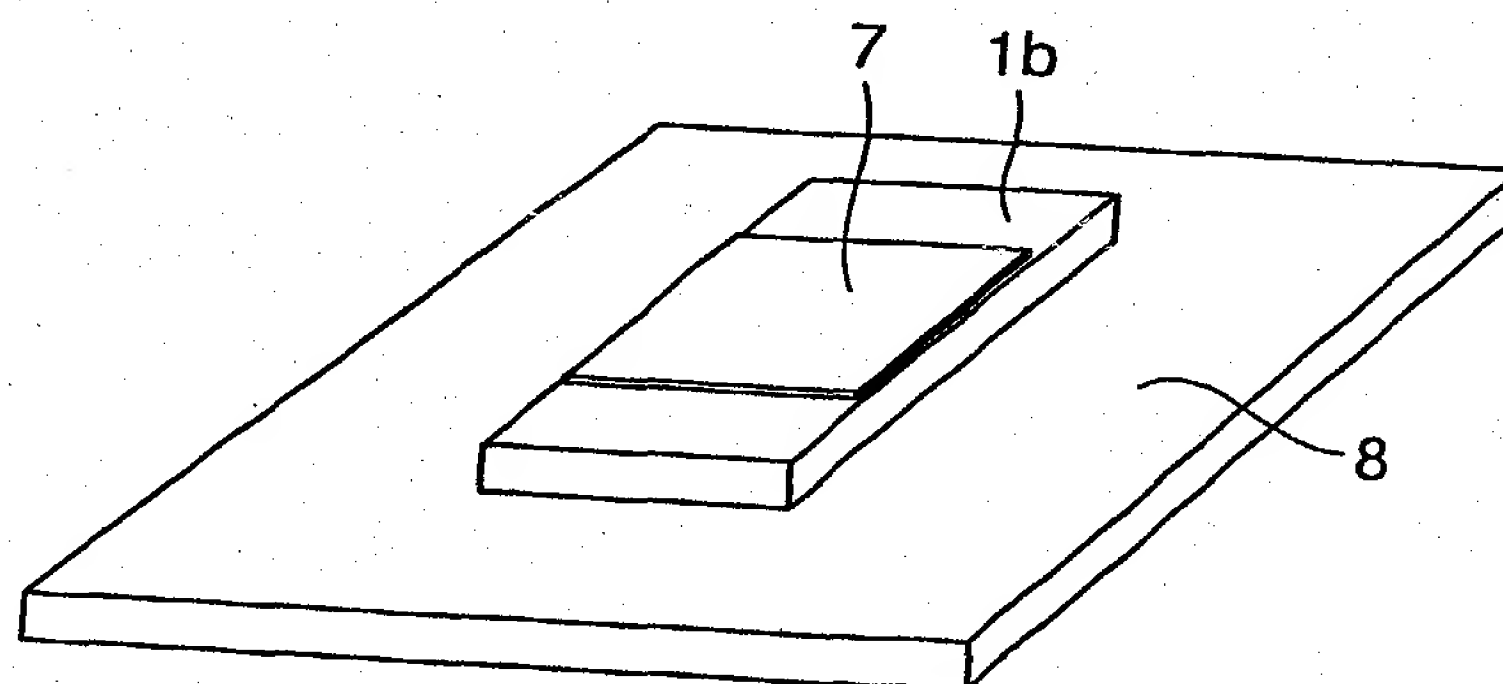


FIG. 4

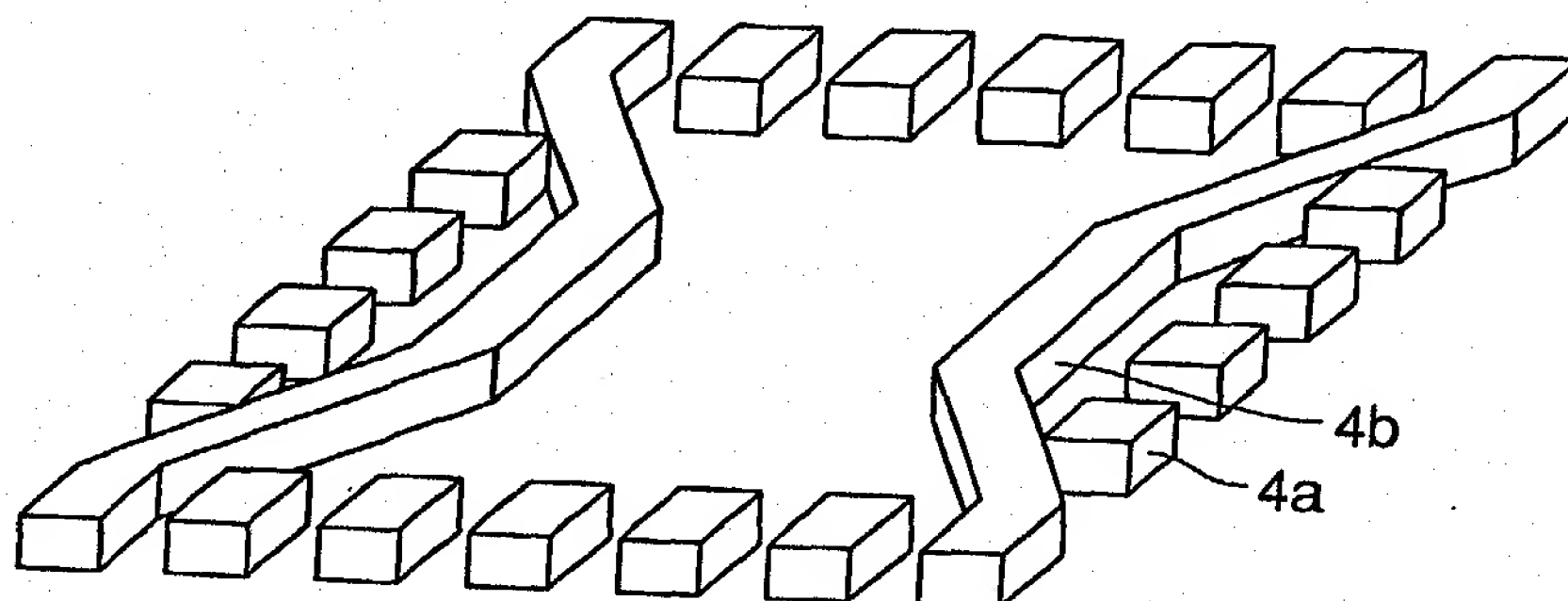


FIG. 5

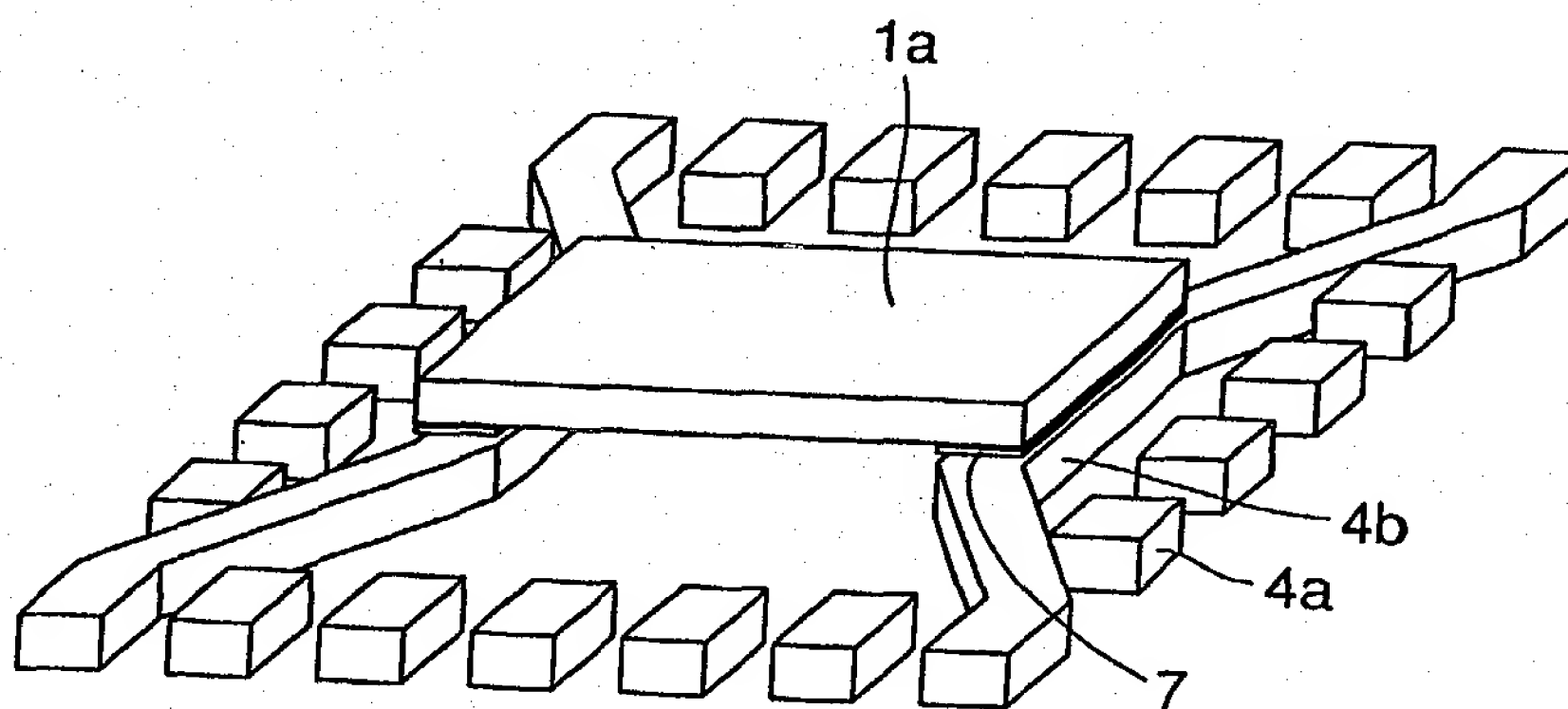


FIG. 6

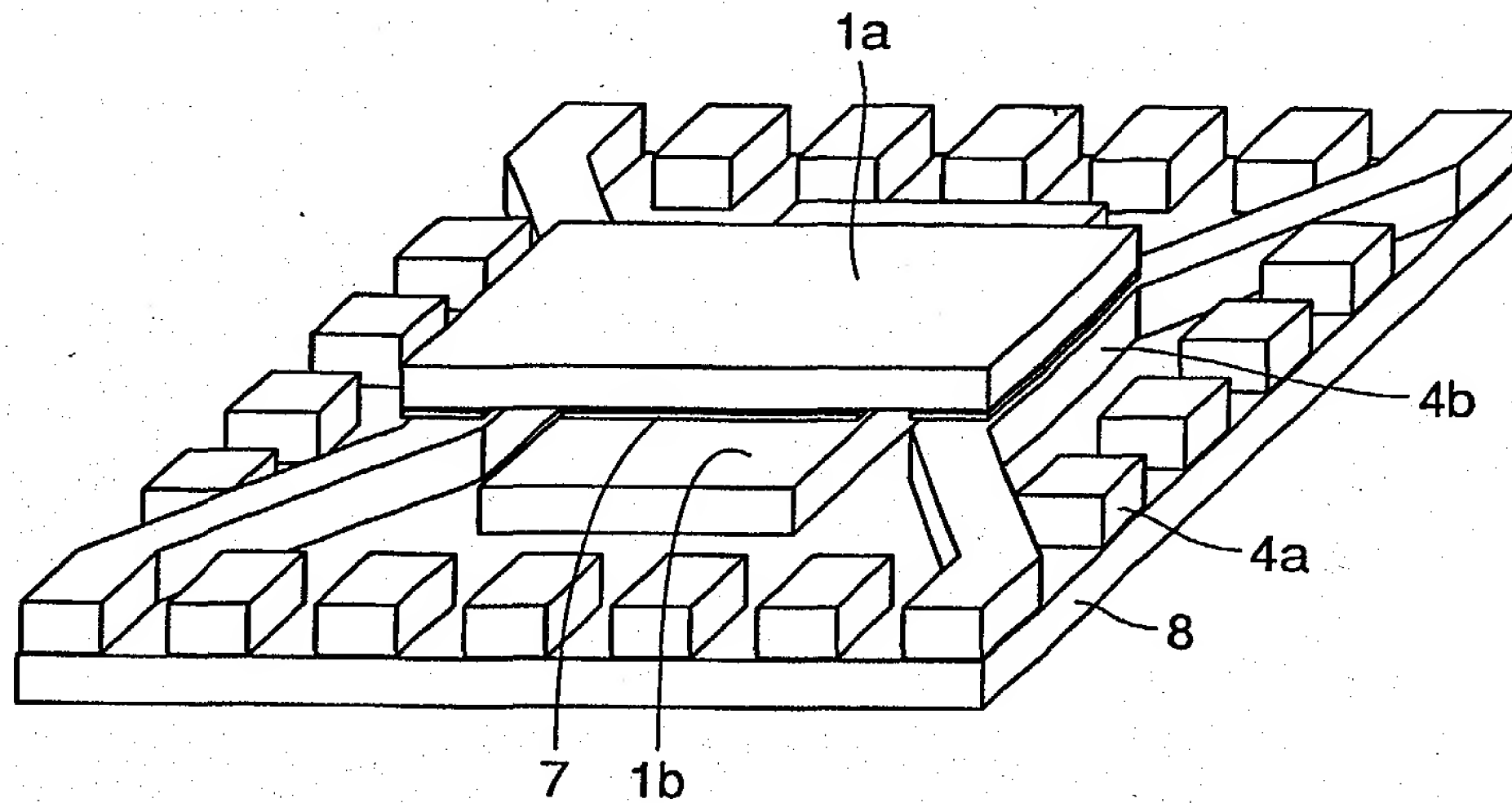


FIG. 7

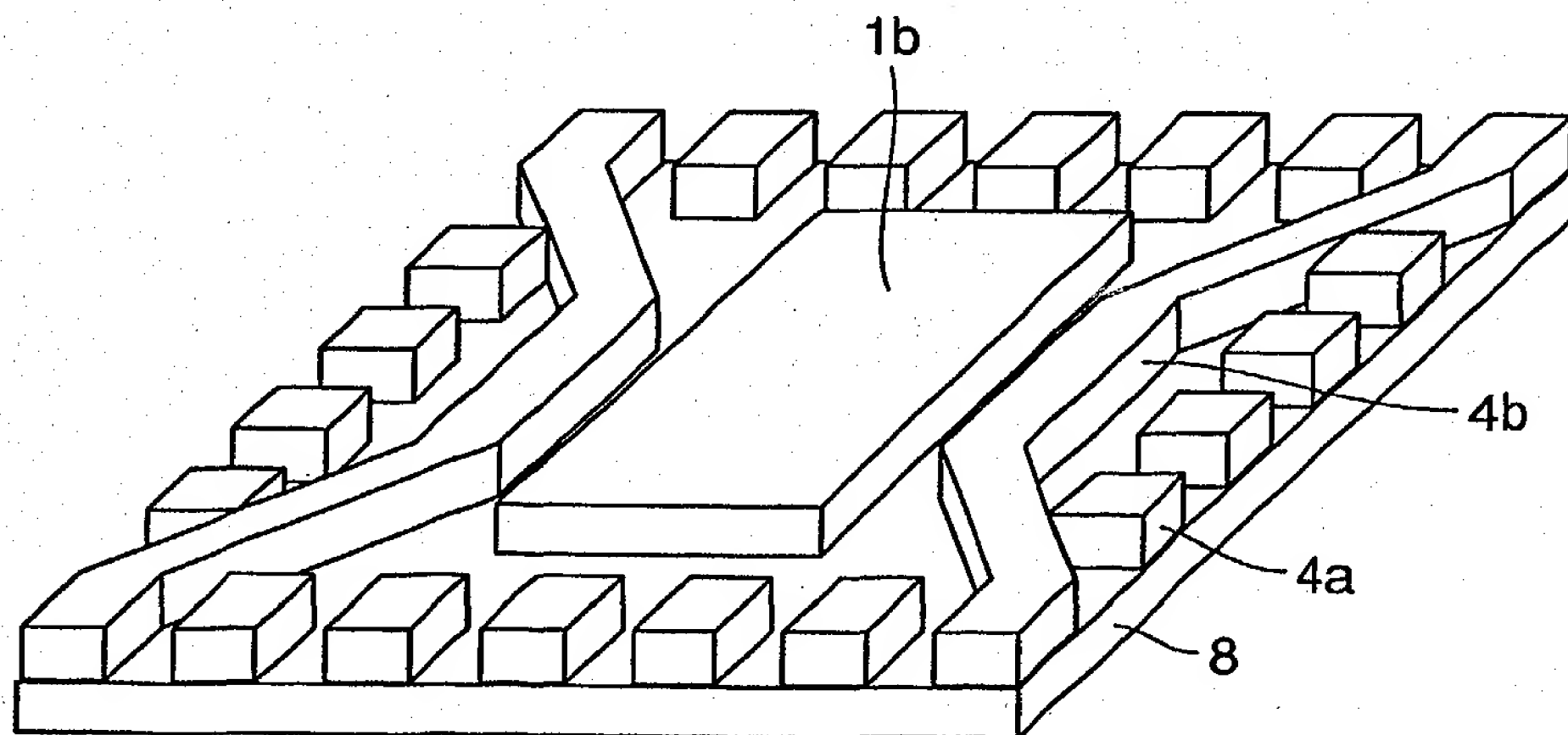
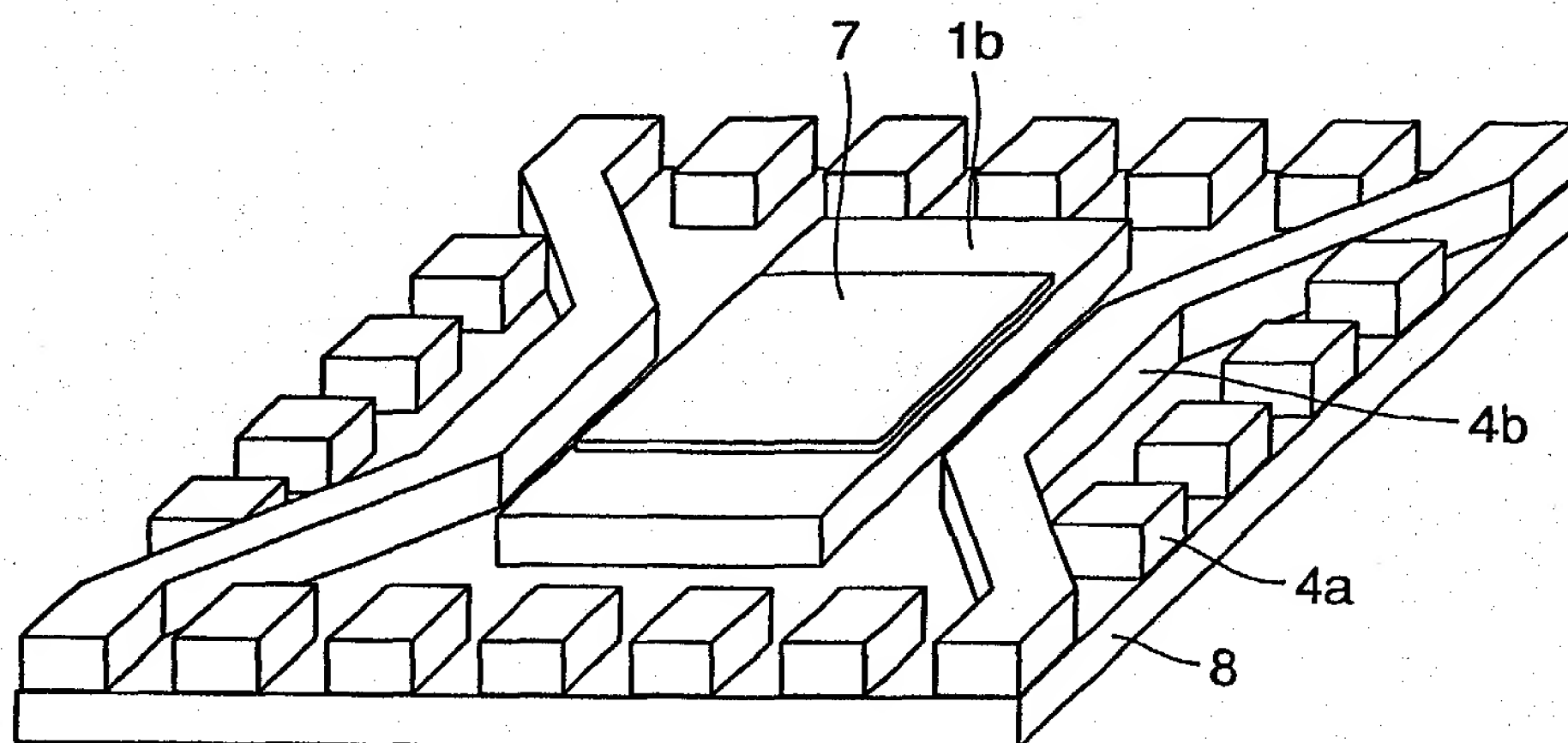


FIG. 8



DATE	DESCRIPTION	AMOUNT	BALANCE
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2026	...		

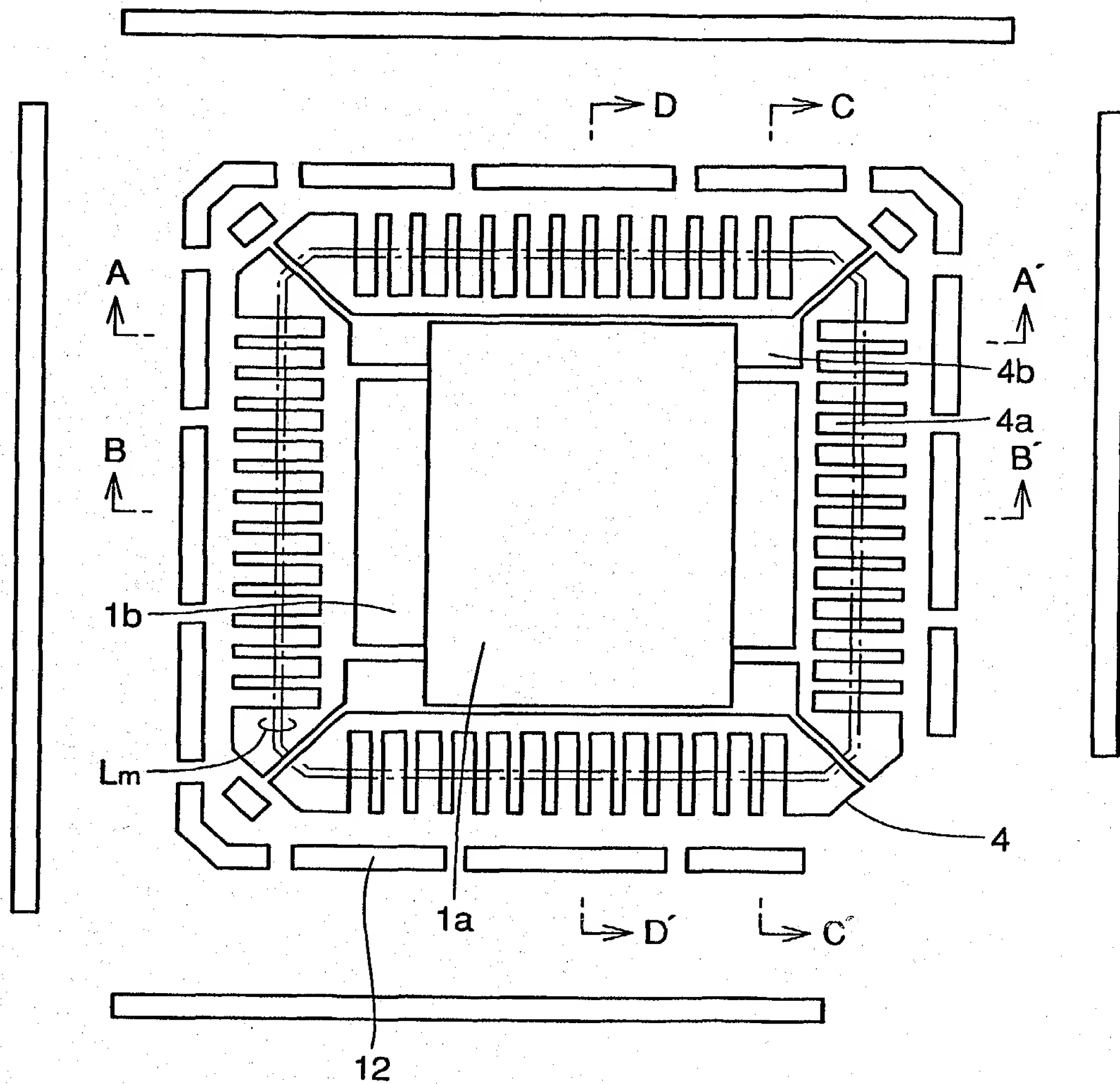


FIG. 10

A-A'

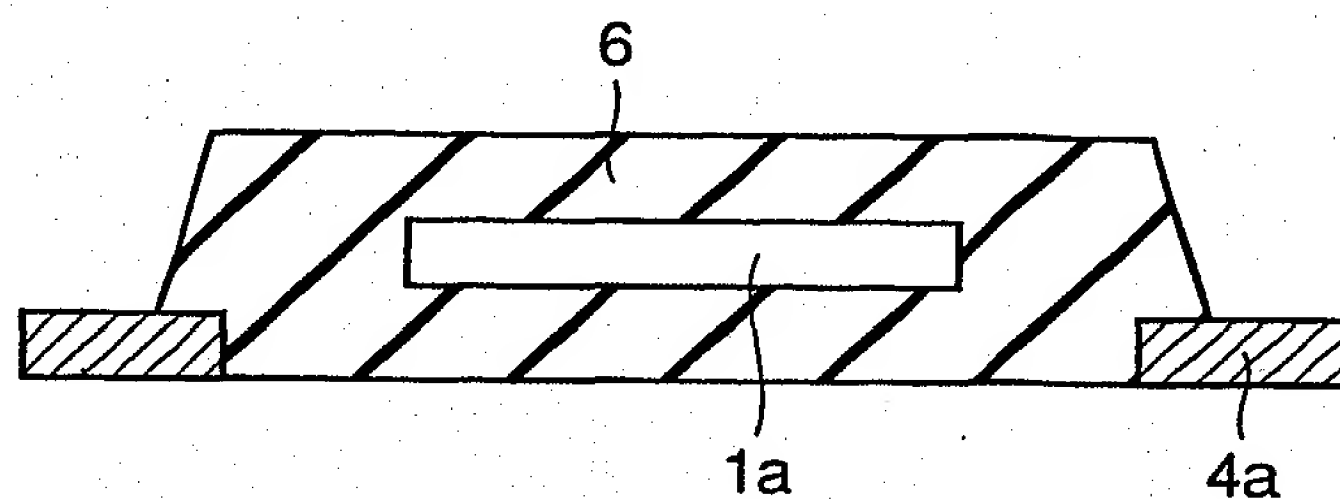


FIG. 11

B-B'

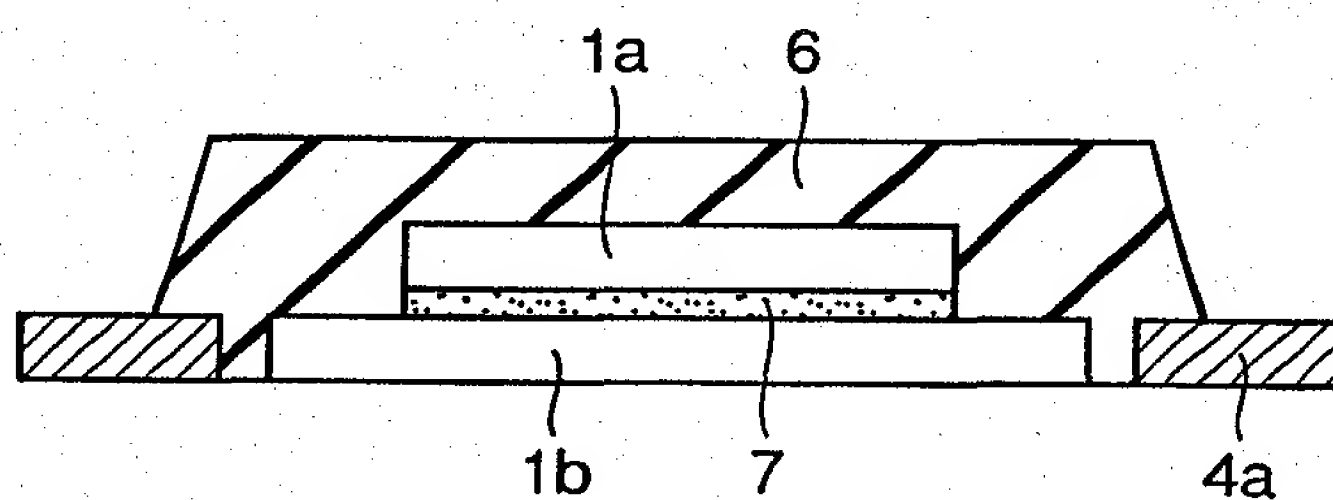


FIG. 12

C-C'

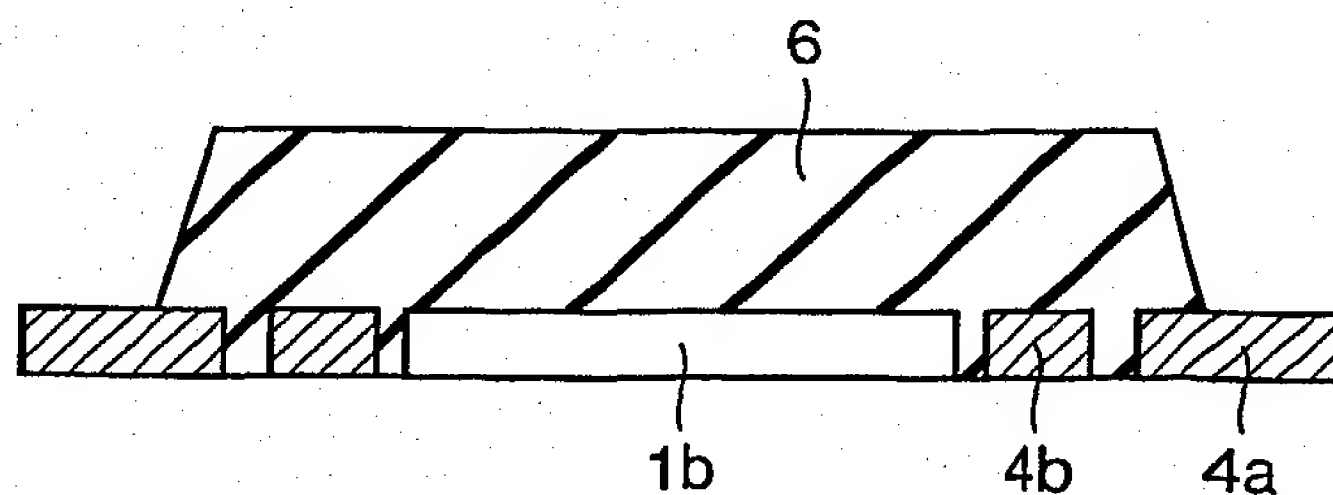


FIG. 13

D-D'

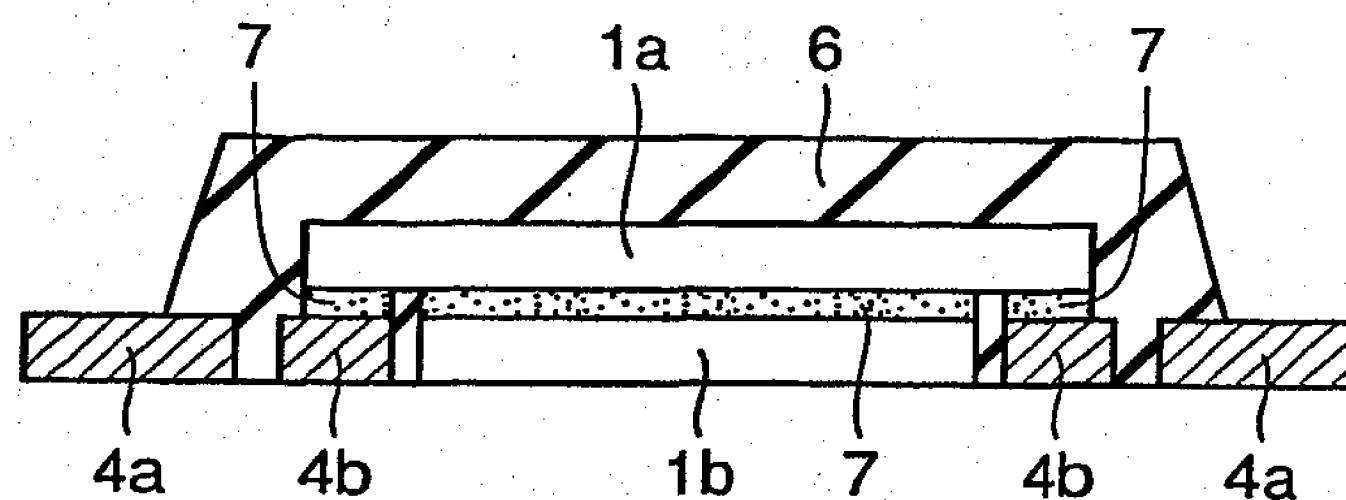


FIG. 14

A-A'

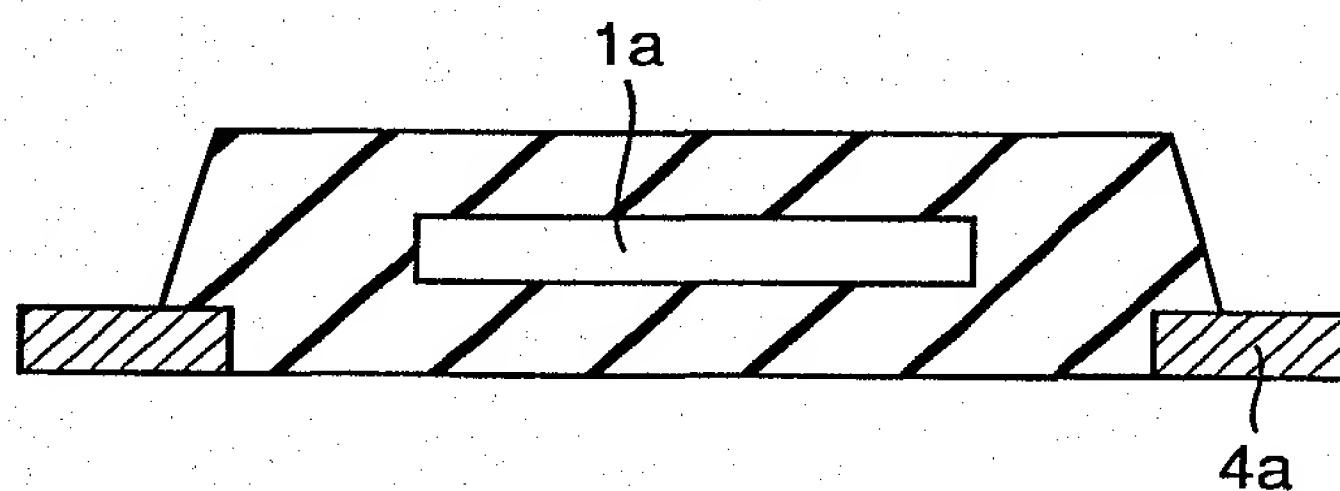


FIG. 15

B-B'

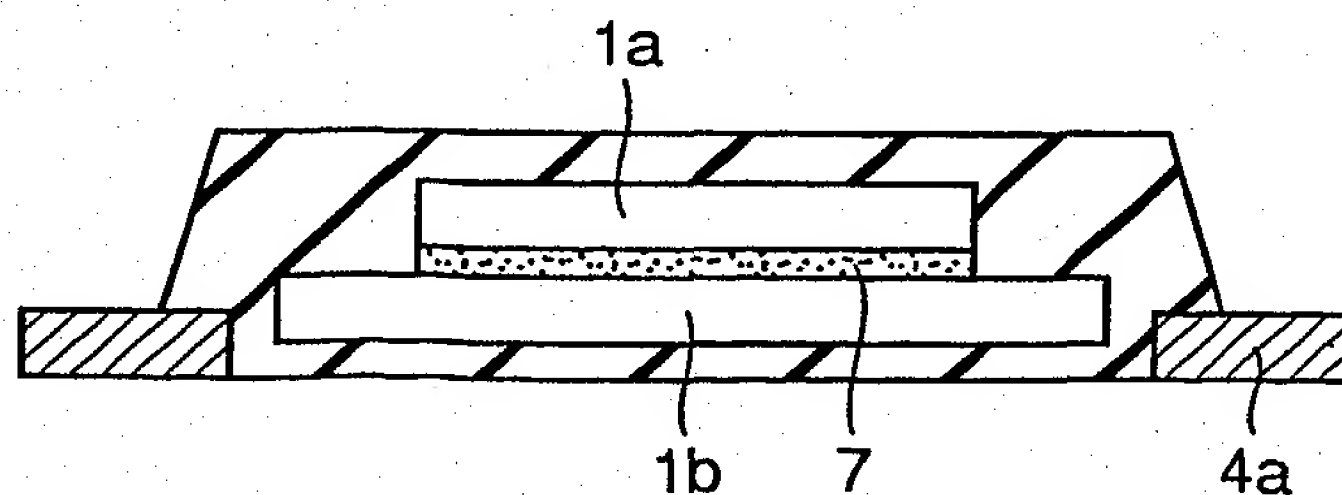


FIG. 16

C-C'

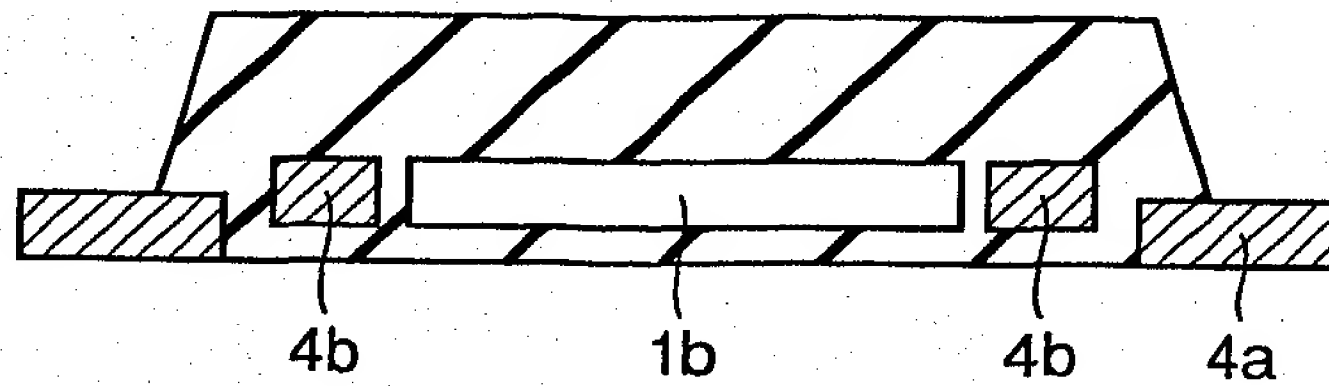


FIG. 17

D-D'

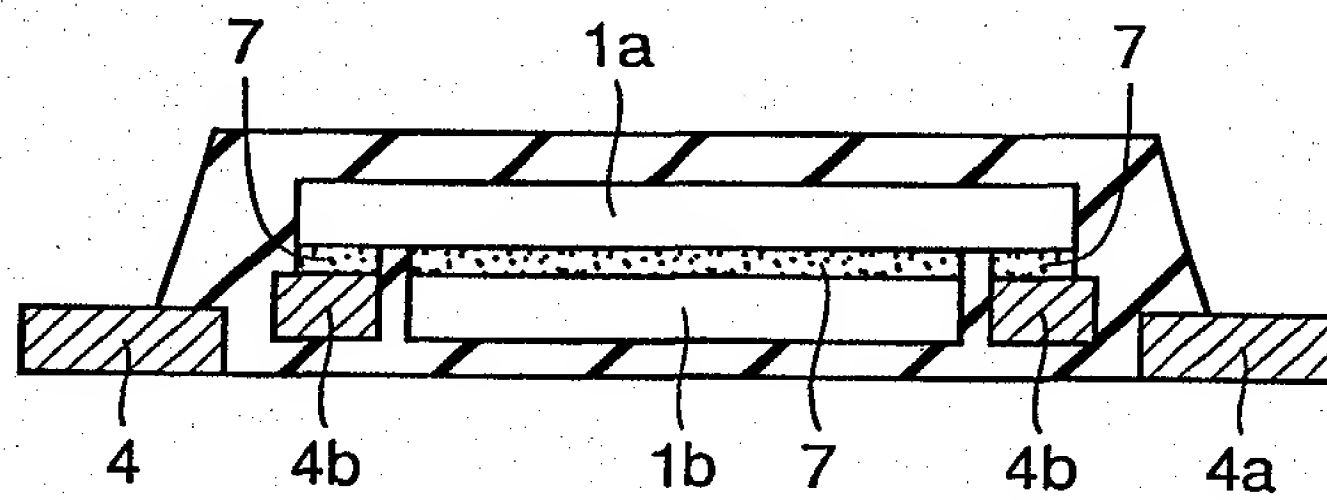


FIG. 18

A-A'

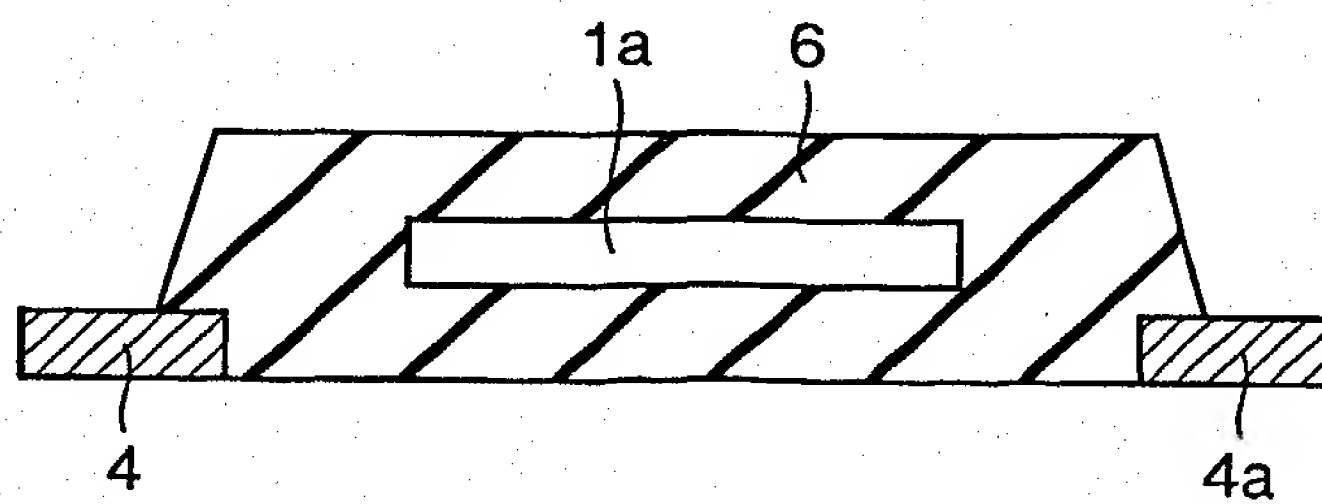


FIG. 19

B—B'

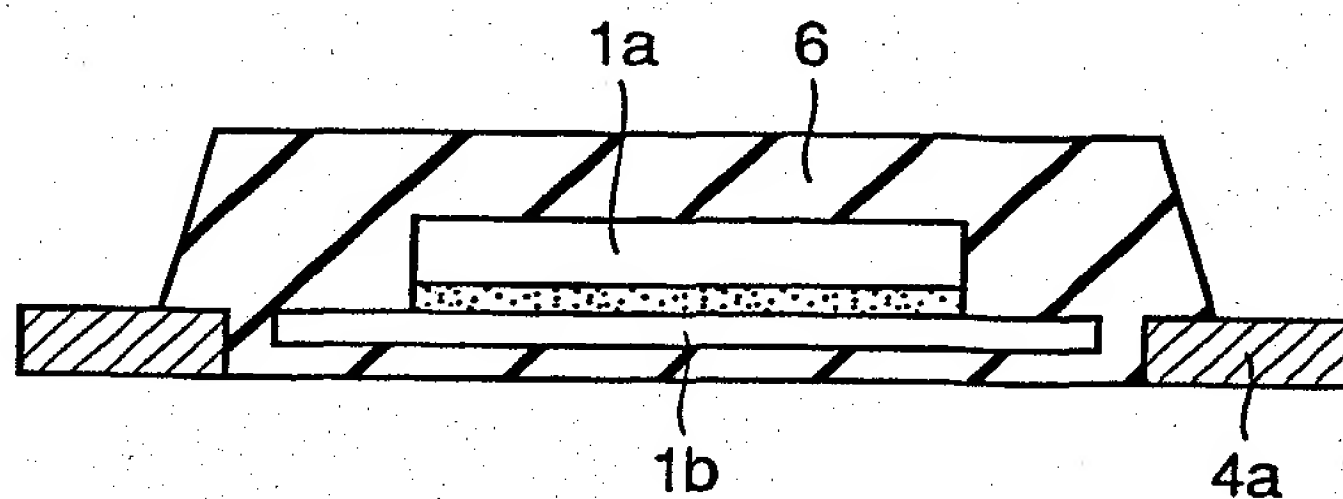


FIG. 20

C—C'

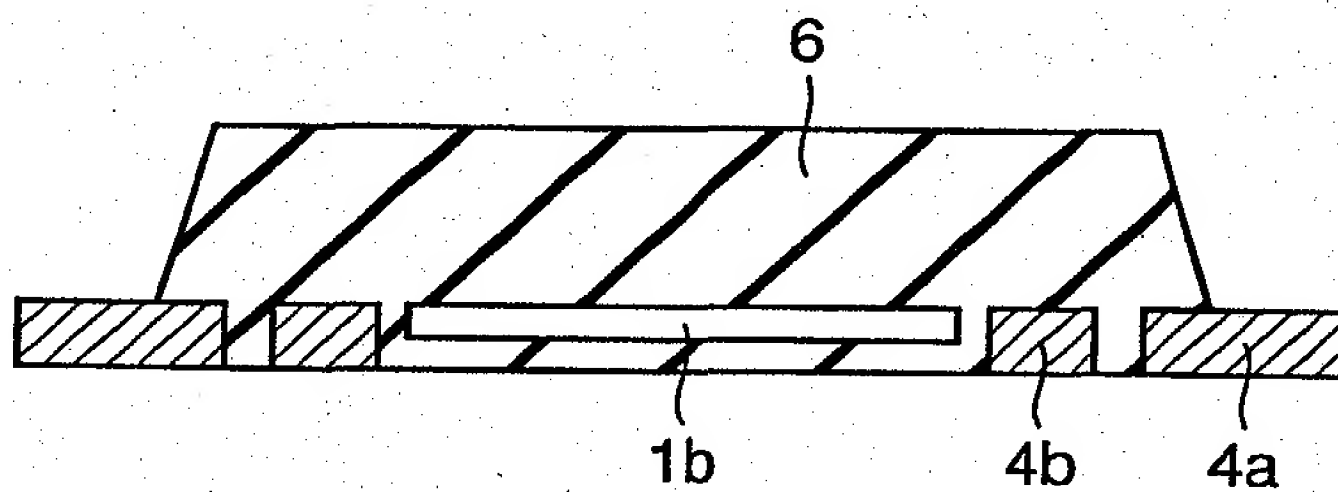


FIG. 21

D—D'

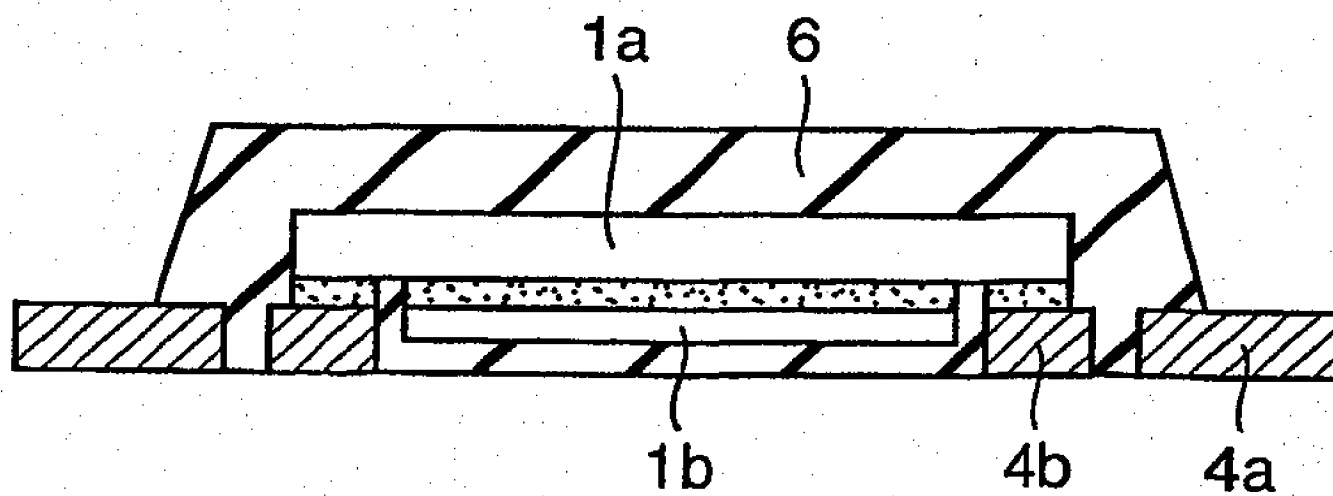


FIG. 22

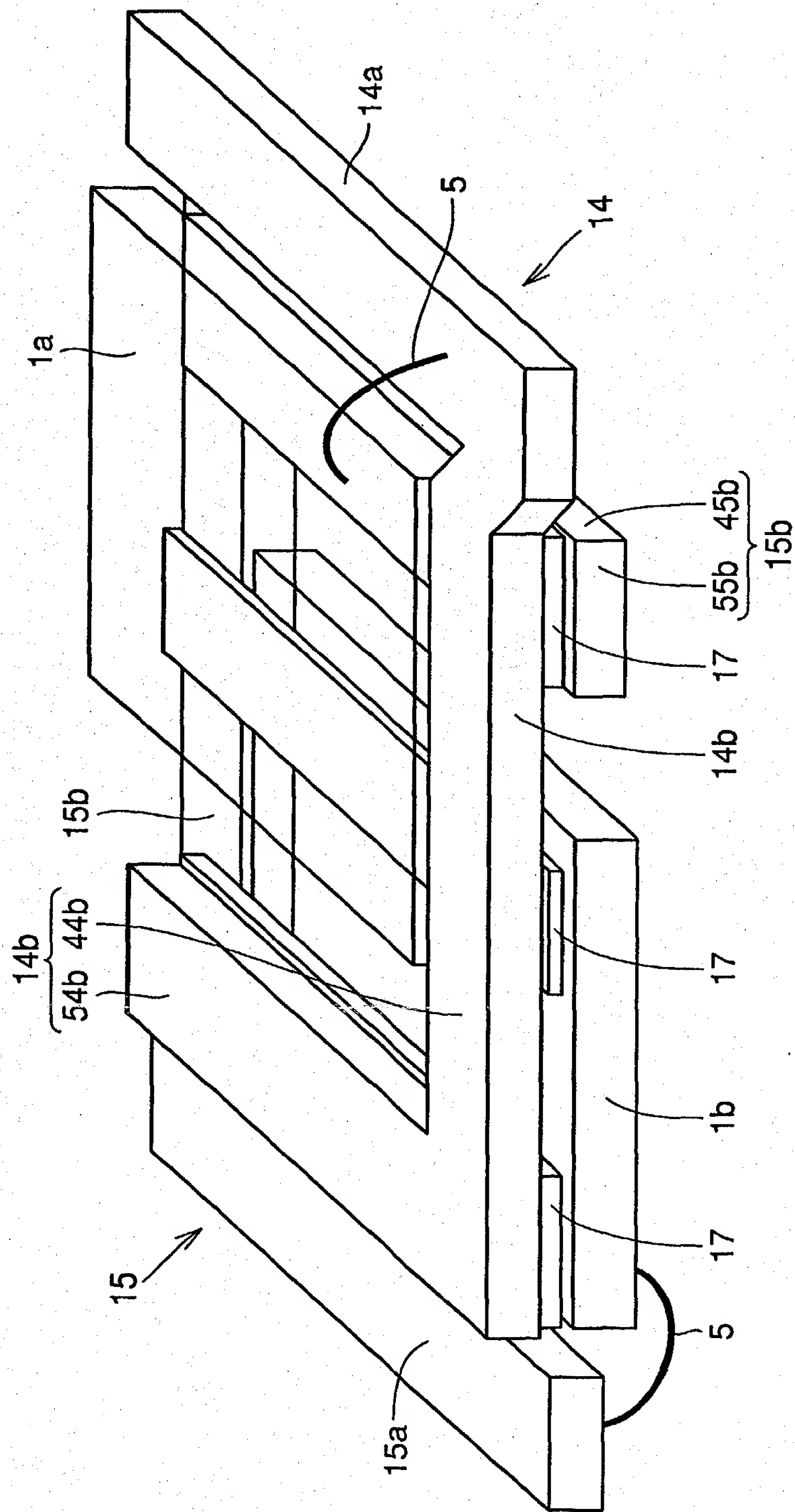


FIG. 24

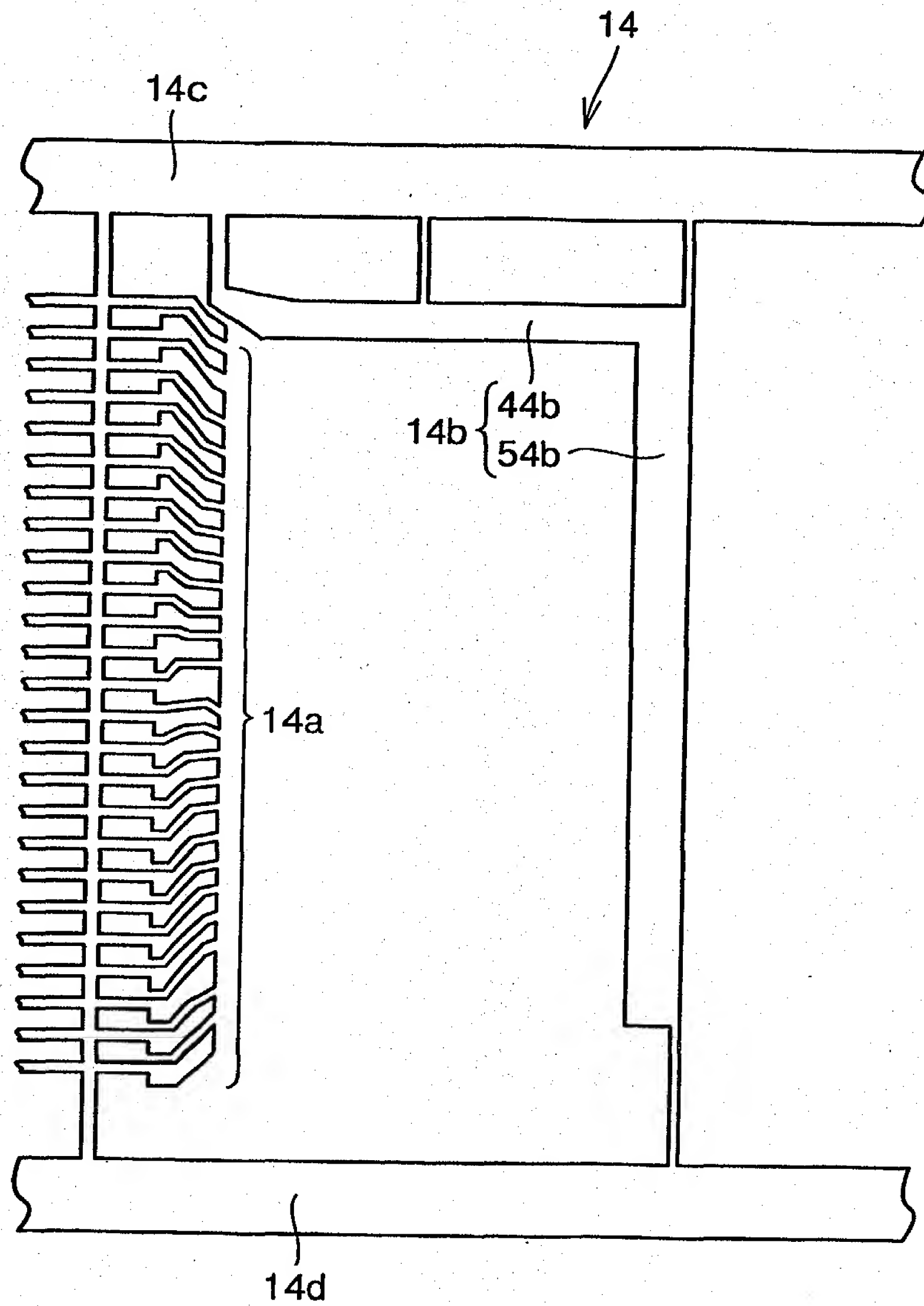


FIG. 25

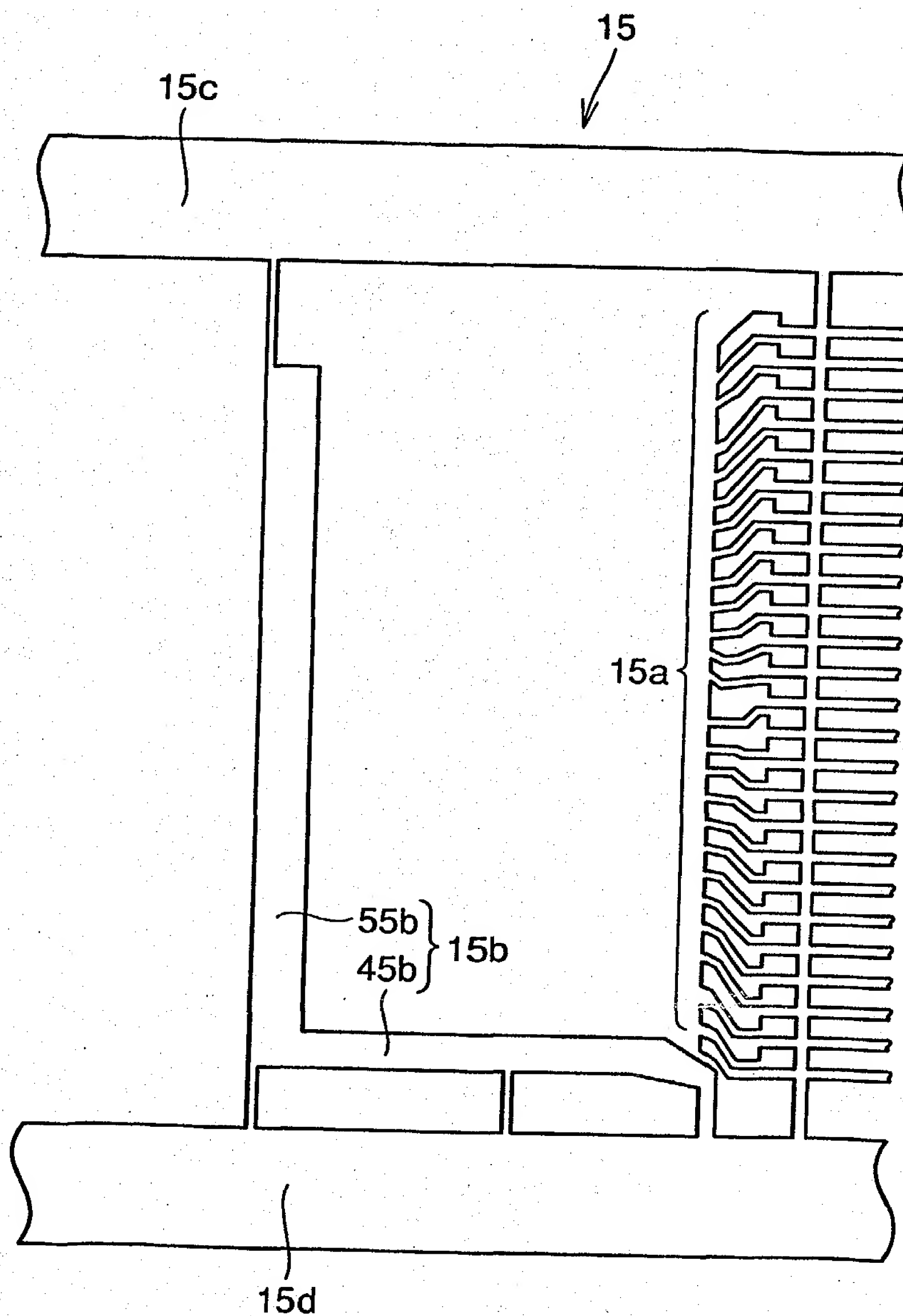


FIG. 26

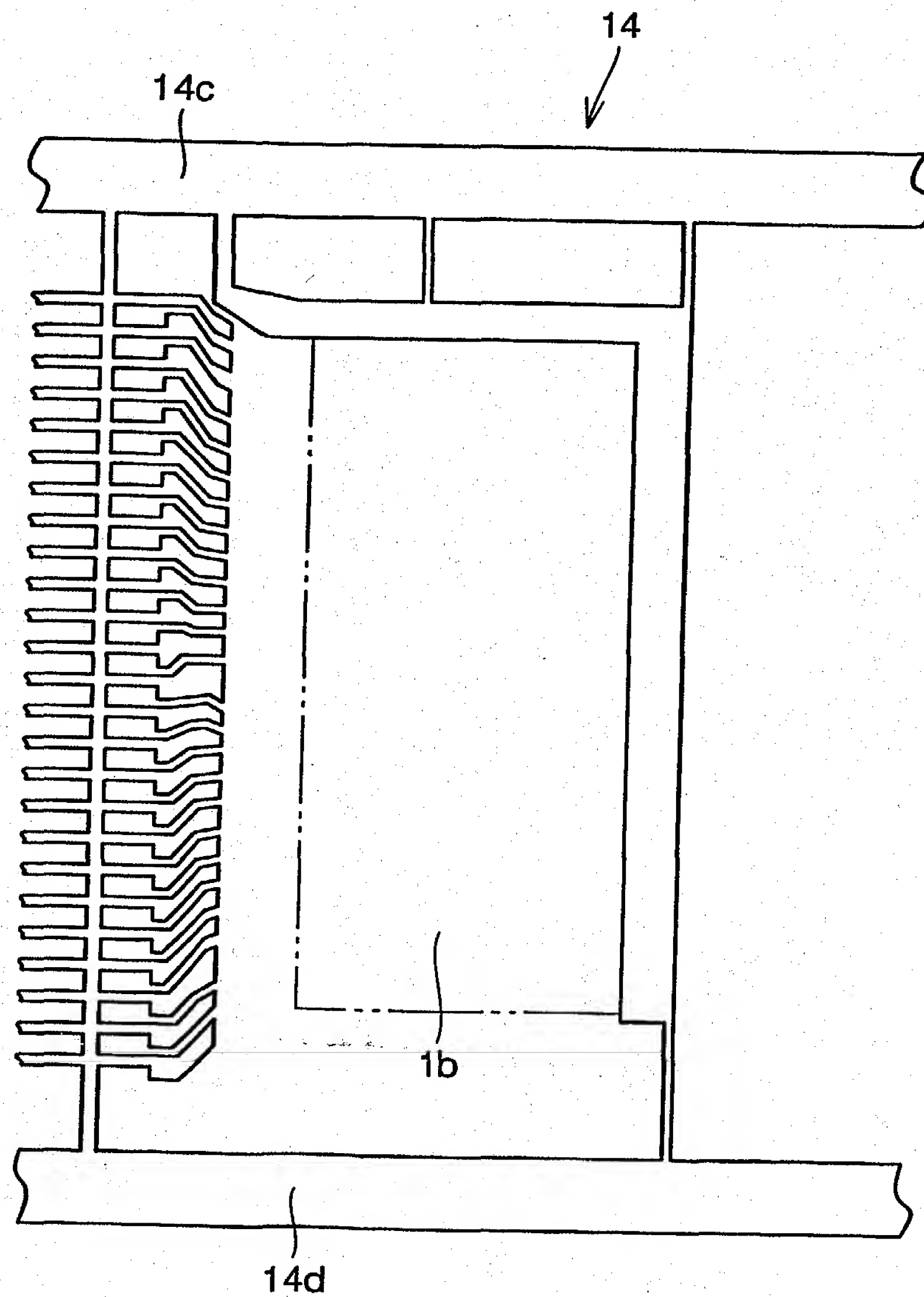


FIG. 27

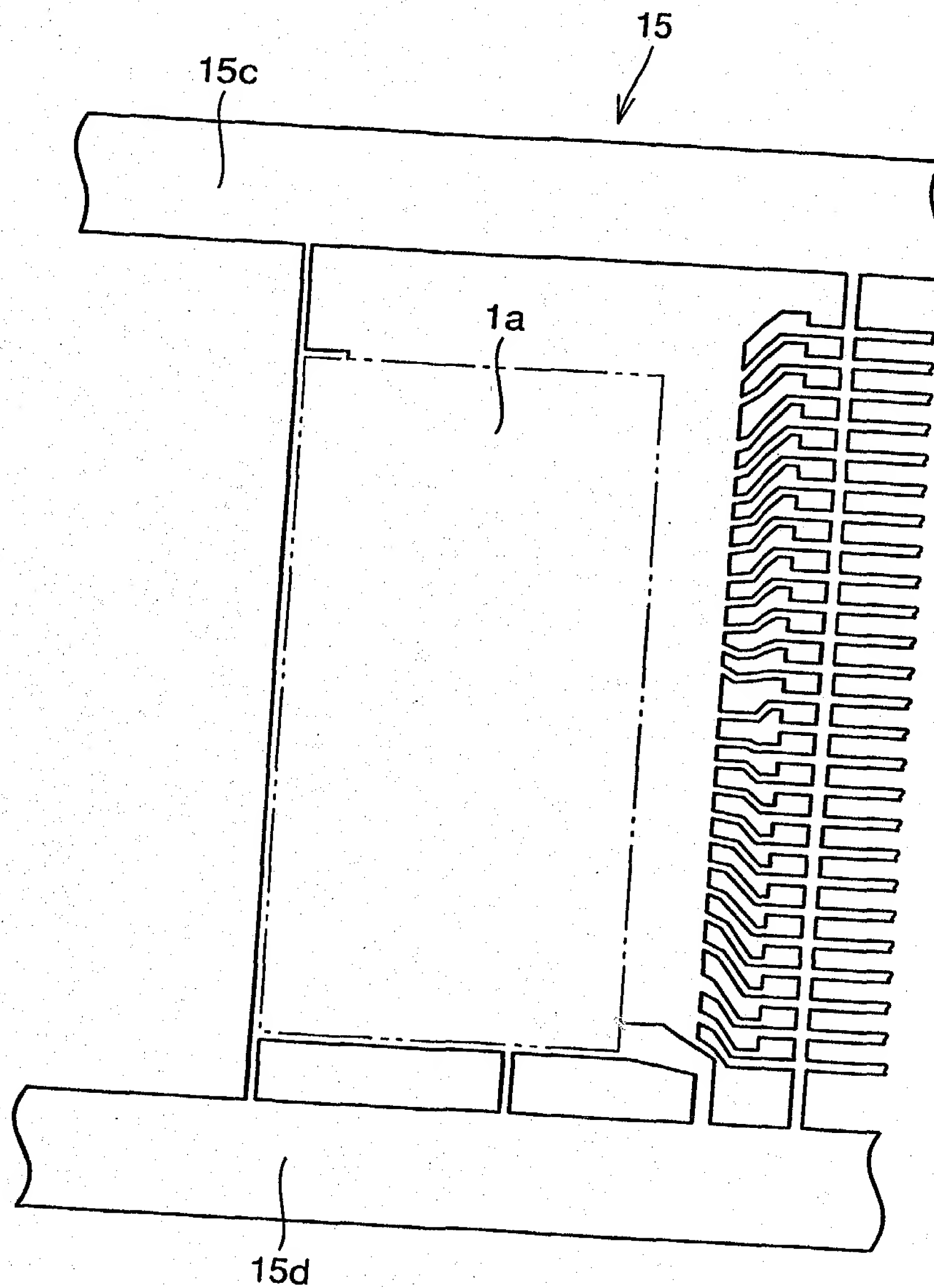


FIG. 28

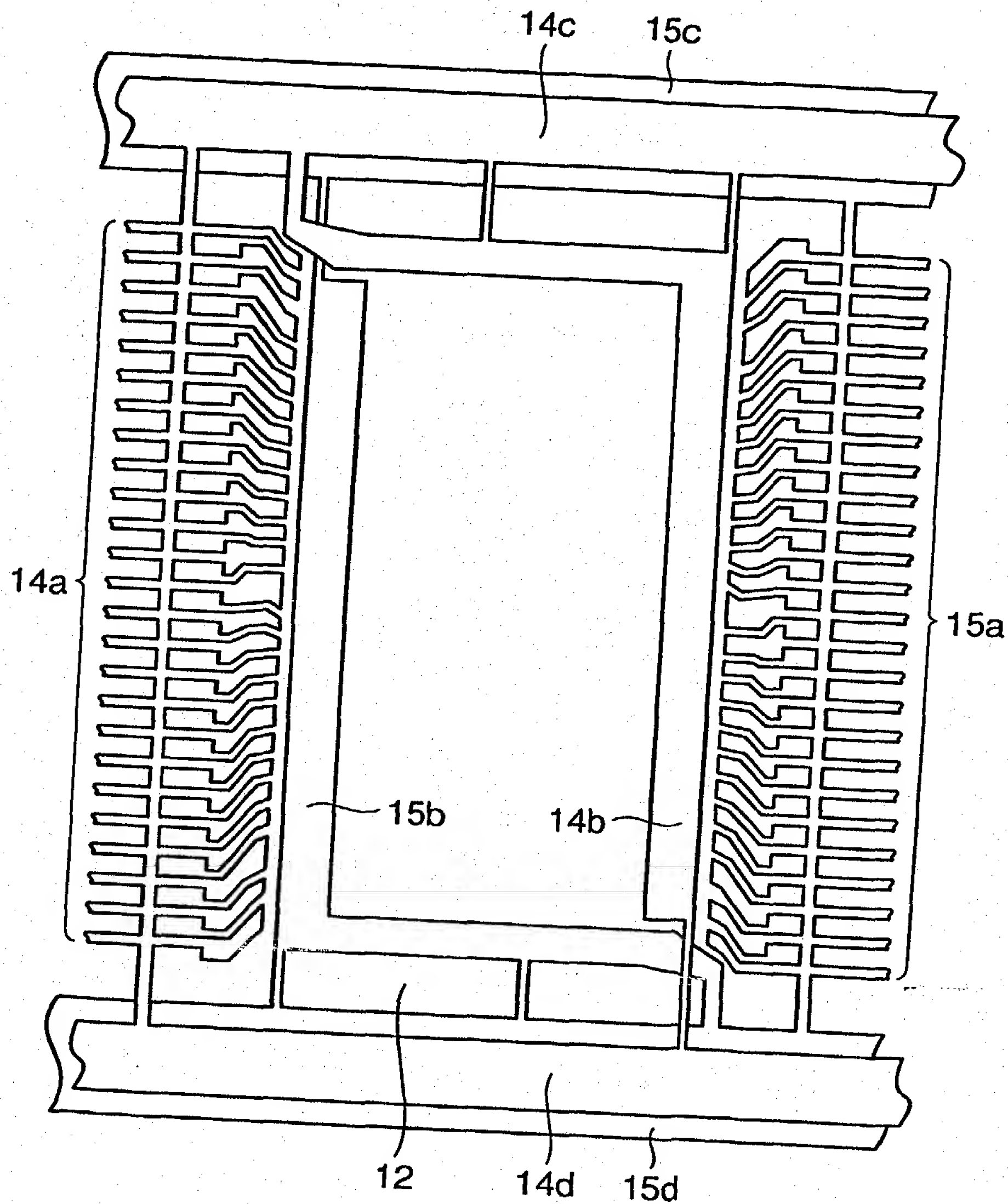


FIG. 29

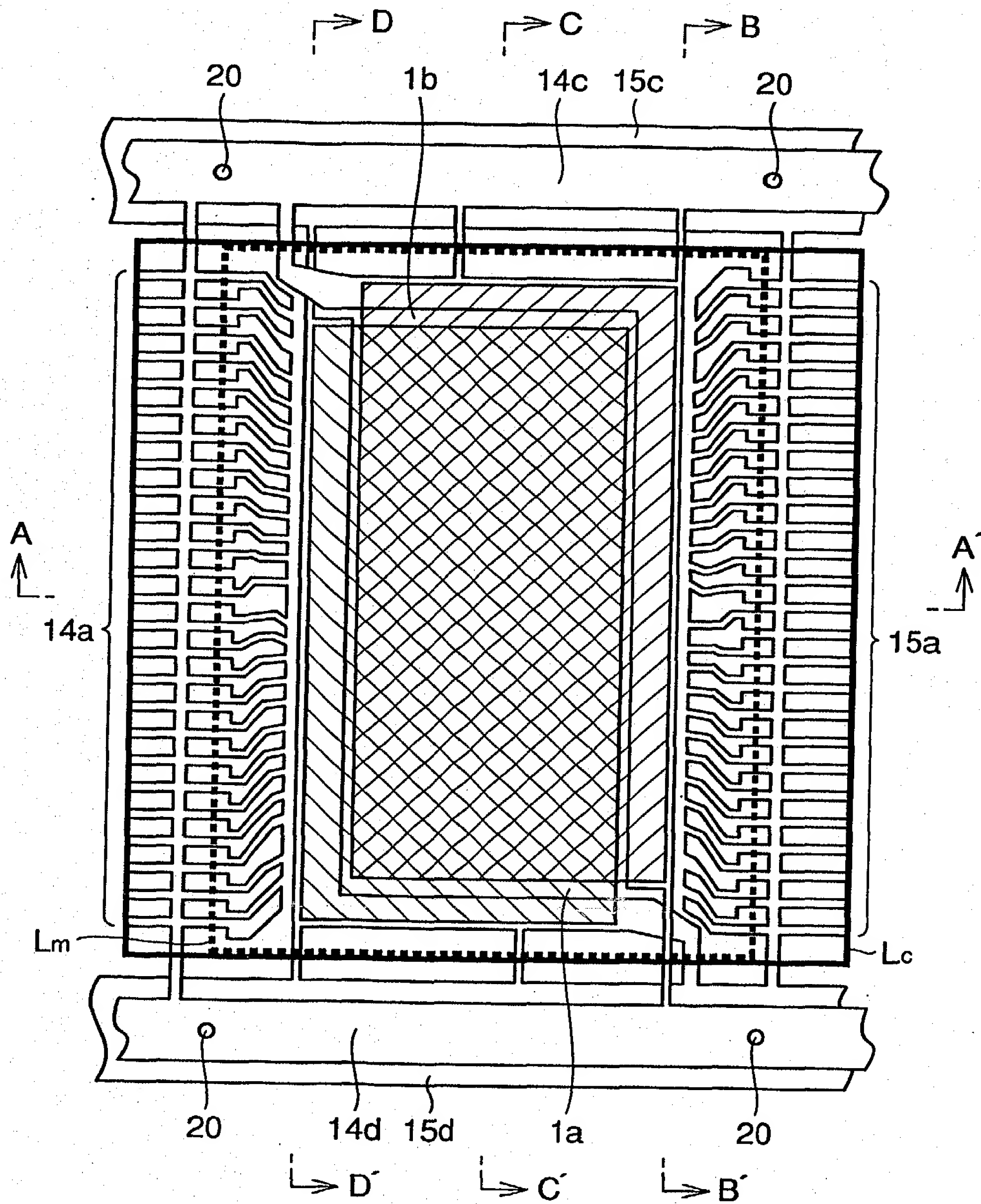
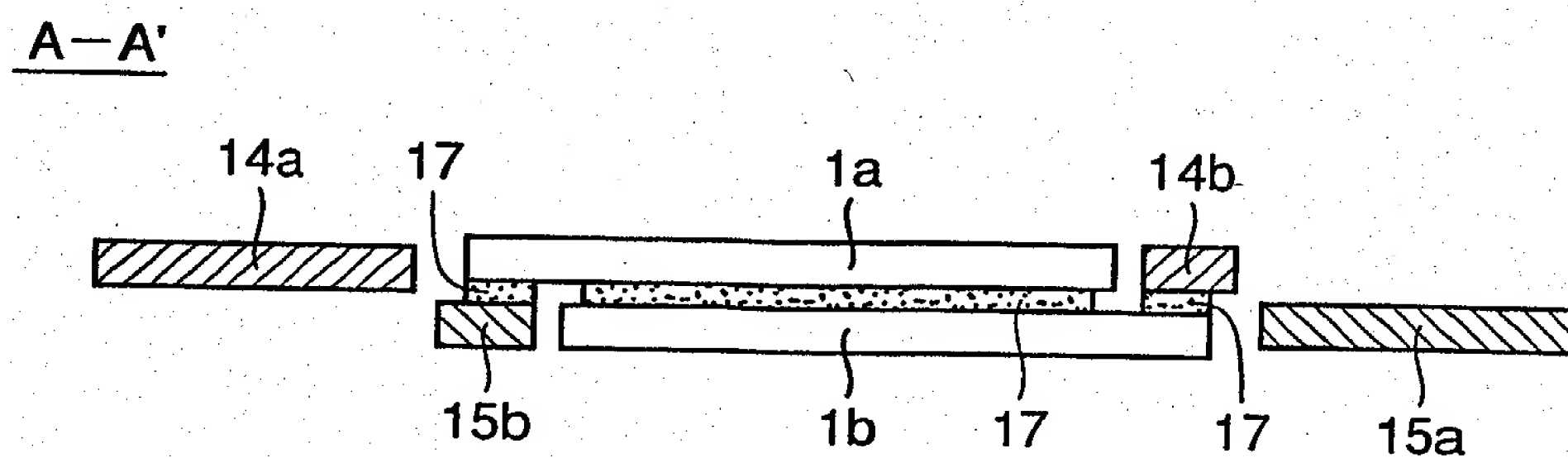


FIG. 30



Title: SEMICONDUCTOR DEVICE AND MANUFACTURING METHOD THEREOF

Inventors: ABE ET AL.

Atty Docket No.: 401263

Leydig, Voit & Mayer, Ltd.

202-737-6770

FIG. 31

B-B'

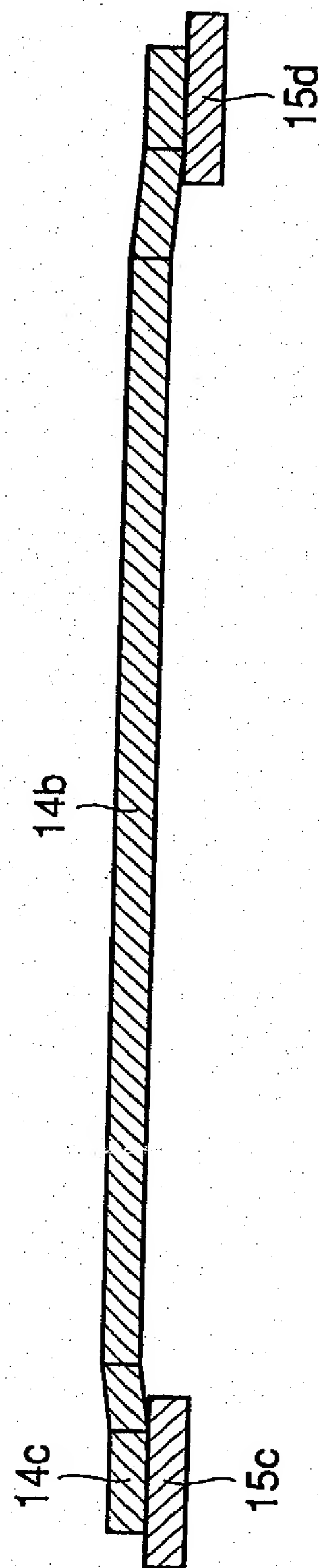


FIG. 32

C-C'

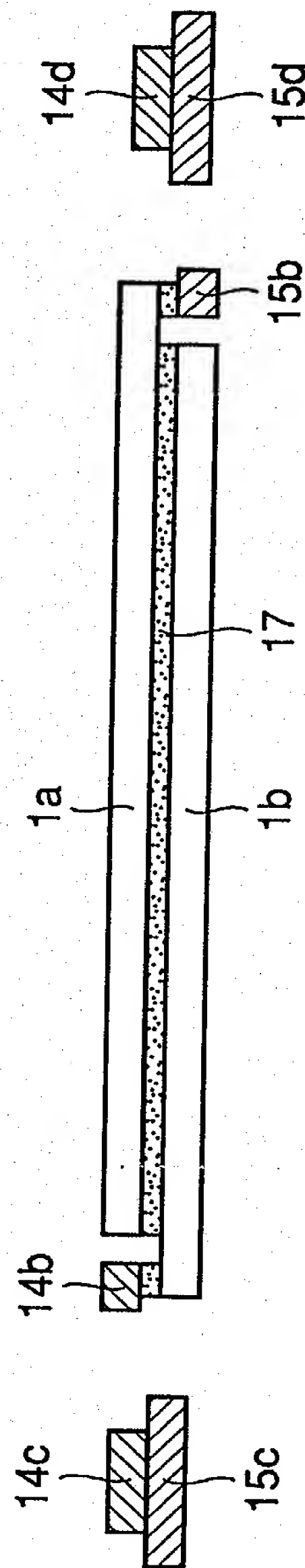


FIG. 33

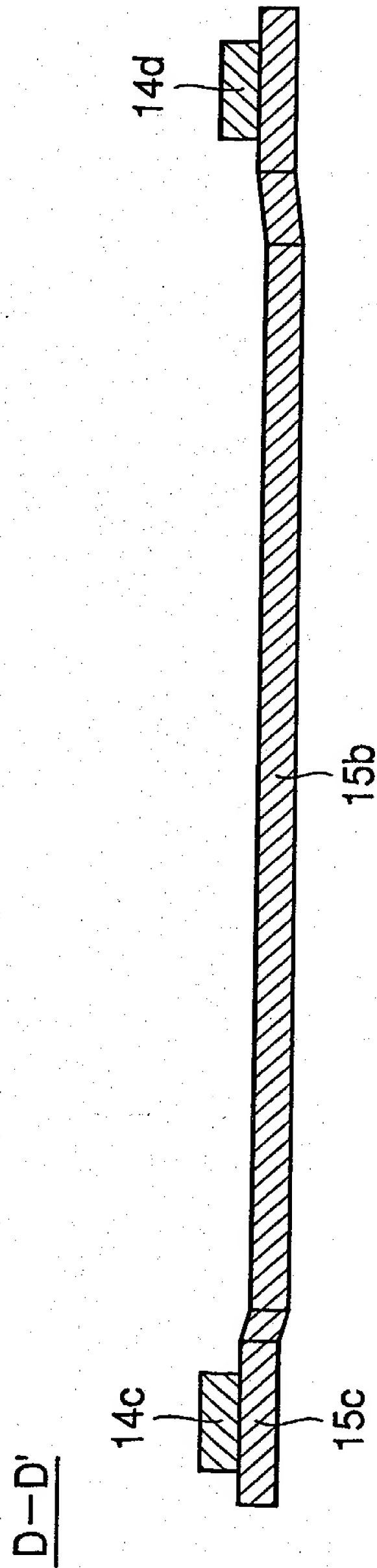


FIG. 34

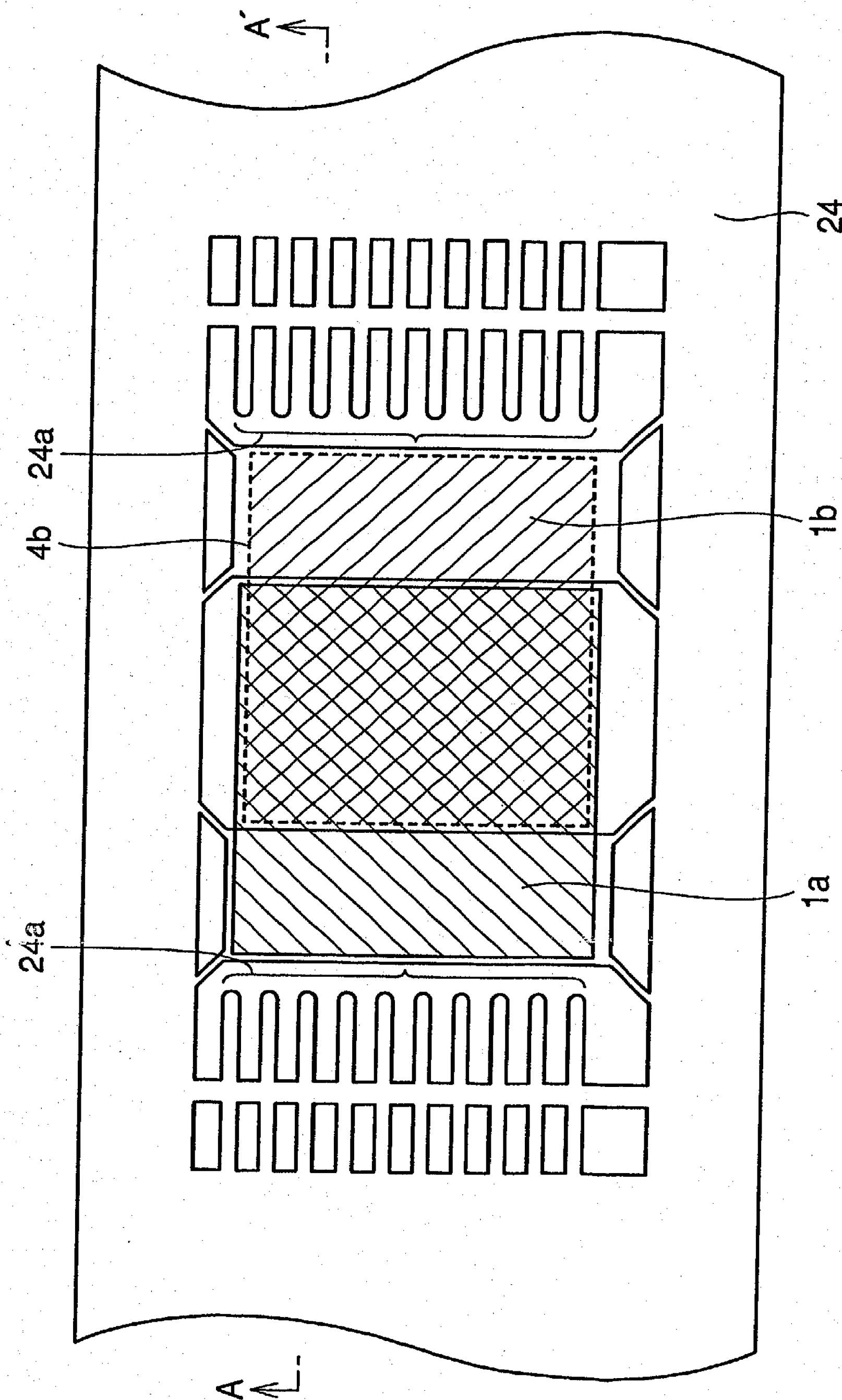


FIG. 35

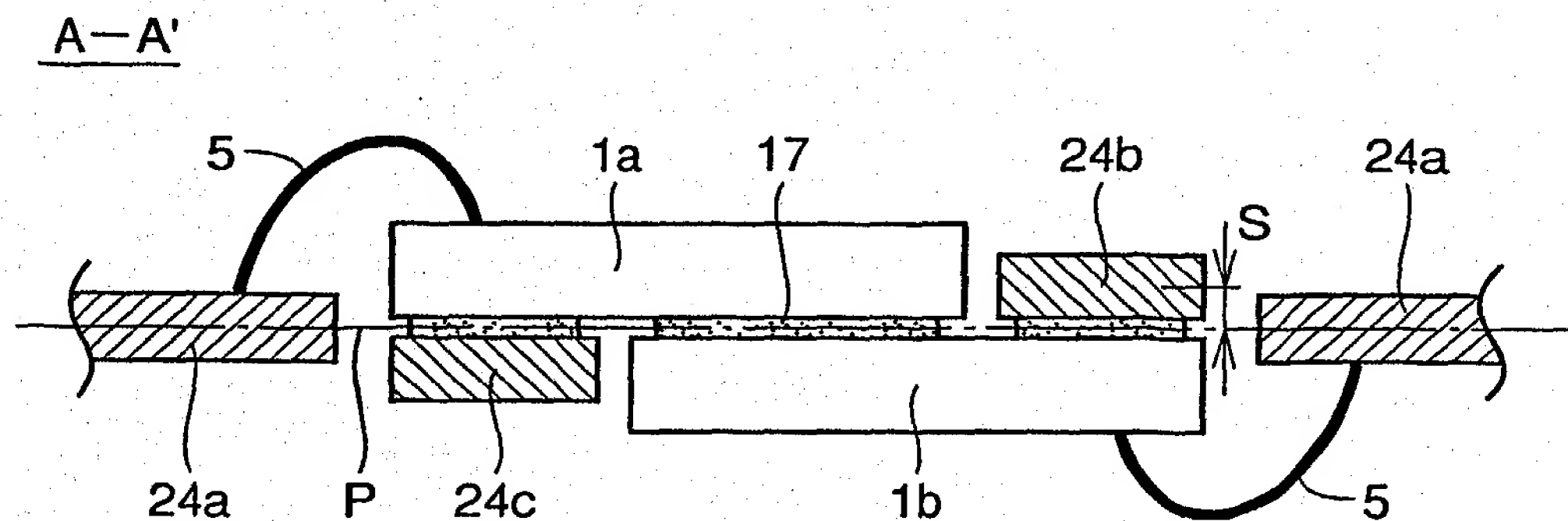


FIG. 36 PRIOR ART

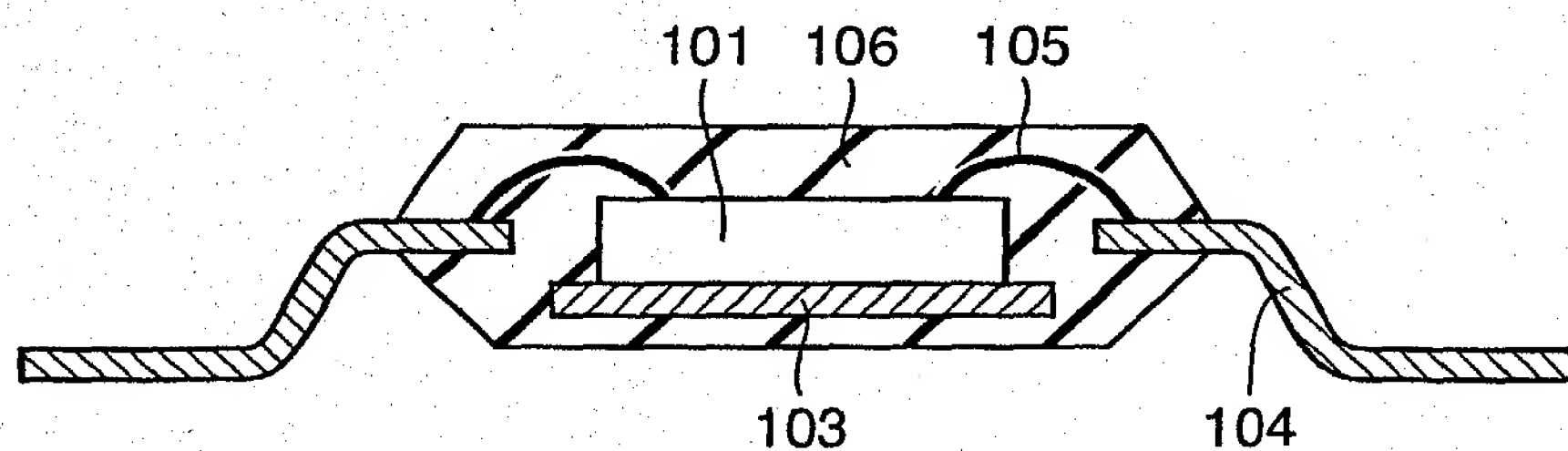


FIG. 37 PRIOR ART

